Chip back potential is the level which bulk silicon is maintained by on-chip connection, or it is the level to which the chip back must be connected when specifically stated below. If no potential is given the chip back should be isolated.

**.011”**

**ANODE**

**.0033”**

**.011”**

**Top Material: Al**

**Backside Material: Au**

**Bond Pad Size: .0033” X .0033”**

**Backside Potential: Cathode**

**Mask Ref: CPD83V**

**APPROVED BY: DK DIE SIZE .011” X .011” DATE: 6/6/23**

**MFG: CENTRAL SEMI THICKNESS .011” P/N: 1N914**

**DG 10.1.2**

#### Rev B, 7/19/02